



PATENT  
2078-3-02

#2  
D-Scott  
5/19/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jin-Koo Rhee  
Hyun-Sik Park  
Dan An  
Yeon-Sik Chae

Serial No: 09/755,436

Filed: January 4, 2001

For: TRANSISTOR WITH  $\pi$  GATE STRUCTURE  
AND METHOD FOR PRODUCING THE SAME

Art Unit:

Examiner: --

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

Assistant Commissioner for Patents  
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Date of Deposit

Jonathan Y. Kang

Name

2/22/2001

Signature

Date

TRANSMITTAL OF PRIORITY DOCUMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Enclosed herewith is a certified copy of Korean patent application No. 2000-70982 which was filed on November 27, 2000 from which priority is claimed under 35 U.S.C. Section 119 and Rule 55.

Acknowledgment of the priority documents (s) is respectfully requested to ensure that the subject information appears on the printed patent.

Respectfully submitted,

Date: February 22, 2001

By:

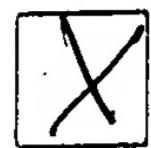
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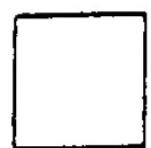
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## ARTIFACT SHEET

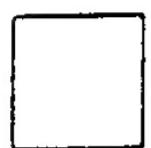
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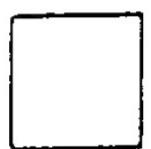
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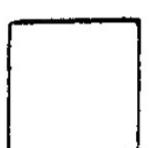
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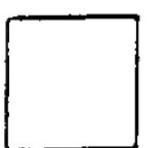
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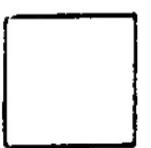
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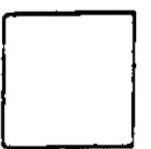
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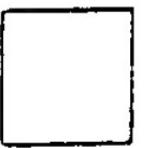
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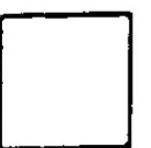
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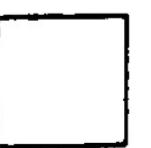
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